

PAT-NO: JP402242230A
DOCUMENT-IDENTIFIER: JP 02242230 A
TITLE: LIQUID CRYSTAL DISPLAY DEVICE
PUBN-DATE: September 26, 1990

INVENTOR-INFORMATION:

NAME

HAYASHI, SHINICHIRO

YAMAMOTO, ATSUYA

SENDA, KOJI

FUJII, EIJI

EMOTO, FUMIAKI

ASSIGNEE-INFORMATION:

NAME

MATSUSHITA ELECTRON CORP

COUNTRY

N/A

APPL-NO: JP01062048

APPL-DATE: March 16, 1989

INT-CL (IPC): G02F001/136, G02F001/133 , G09F009/30

US-CL CURRENT: 349/143

ABSTRACT:

PURPOSE: To uniformize the spacing between two substrates over the entire surface so as to uniformize the thickness of a liquid crystal to be sealed and to improve display quality by providing a pseudo perpendicular scanning circuit and pseudo horizontal scanning circuit so as to respectively face a perpendicular scanning circuit and a horizontal scanning circuit.

CONSTITUTION: Gate signal lines 2, source signal lines 3 and bonding pads 4 are formed on the quartz substrate 1 and a picture element part 10 having thin-film transistors, the perpendicular scanning circuit 11 and

the horizontal scanning circuit 12 are formed. The picture element 10 is simultaneously interposed and the pseudo perpendicular scanning circuit 13 and the pseudo horizontal scanning circuit 14 respectively facing the perpendicular and horizontal scanning circuits 11, 12 are formed. A liquid crystal oriented film is thereafter formed on the picture element part 10 and is subjected to a rubbing; thereafter, the liquid crystal is sealed between this substrate and a front surface glass plate formed with a common electrode. The nonuniform spacing between the front surface glass plate and the quartz substrate is then uniformized by the pseudo perpendicular scanning circuit 13 and horizontal scanning circuit 14 and the thickness of the sealed liquid crystal is uniformized.

COPYRIGHT: (C)1990,JPO&Japio

BEST AVAILABLE COPY

G09F 9/30 338 7610-56

BEST AVAILABLE COPY

(13) は断り真第4段の「水平走食経過多」を「適切な水平走食回数」とし訂正する。

特許請求の範囲を記載した書式

特許請求の範囲

構成上、前記一センサより得られる複数の測定値により形成した測定値と、その測定値の角に配置され、測定値を比較し検知する検知手段の両方および水平走査回線を有する検出表示装置において、

前記検出表示装置を介在させて前記検知手段の両方および水平走査回線とそれぞれ対応して形成した、前記検知手段の両方および前記水平走査回線に近接の検知手段走査回線および前記水平走査回線を有することを特徴とする検出表示装置。

BEST AVAILABLE COPY